

Appl. No. : **Unknown**
Filed : **Herewith**

AMENDMENTS TO THE CLAIMS

Please add the following claims:

1 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated.

2 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with an oxidizing agent.

3 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with blowing of ozone to the ground or polished surface thereof.

4 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with ozone water.

5 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with illumination of the ground or polished surface thereof with ultraviolet.

6 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, and then a dicing sheet is adhered to the ground or polished surface of the wafer after the deactivation treatment.

7 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with an oxidizing agent, and then a dicing sheet is adhered to the ground or polished surface of the wafer after the deactivation treatment.

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8 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with blowing of ozone to the ground or polished surface thereof, and then a dicing sheet is adhered to the ground or polished surface of the wafer after the deactivation treatment.

9 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with ozone water, and then a dicing sheet is adhered to the ground or polished surface of the wafer after the deactivation treatment.

10 (original): A wafer back surface treating method in which a ground or polished surface of a semiconductor wafer activated in a grinding or polishing step, with semiconductor circuits formed thereon, is deactivated, wherein the deactivation treatment is implemented with illumination of the ground or polished surface thereof with ultraviolet, and then a dicing sheet is adhered to the ground or polished surface of the wafer after the deactivation treatment.

11 (original): A dicing sheet adhering apparatus having a mechanism to blow ozone to a ground or polished surface of a wafer.

12 (original): A dicing sheet adhering apparatus having a UV illumination mechanism illuminating a UV-setting protective tape on a ground or polished surface of a wafer with ultraviolet and in addition, a mechanism to blow exhaust having cooled a UV lamp to the ground or polished surface of a wafer.

13 (new): A method for treating a back surface of a semiconductor wafer comprising:

- (i) grinding or polishing the back surface of the wafer;
- (ii) treating the ground or polished back surface uniformly with a surface deactivation agent selected from the group consisting of an oxidizing agent, dry ozone, ozone water, and ultraviolet radiation, to oxidize the ground or polished surface; and
- (iii) adhering a dicing sheet to the oxidized surface of the wafer, wherein adhering strength between the back surface and the dicing sheet is reduced as compared with adhering strength achieved without step (ii).

14 (new): The method according to Claim 13, wherein steps (i), (ii), and (iii) are conducted as a unit of processes.

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15 (new): The method according to Claim 13, wherein step (ii) is conducted per wafer.

16 (new): The method according to Claim 13, wherein step (ii) is conducted per group of wafers.

17 (new): The method according to Claim 13, wherein step (ii) is conducted by changing the atmosphere surrounding the wafer.